

NAME: NAIRA NAFEES.

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CARRIER OBJECTIVE: To secure a challenging position in a reputable organization to expand my learning, knowledge and skills.

EDUCATION

M-TECH. Electronics and communication engineering (2020-2023) Shri

Mata Vaishno Devi University, Katra, J&K.

B-TECH. Electronics and communication engineering(2015-2019)

Baba Ghulam Shah Badshah University, Rajouri, J&K(percentage 79.06%).

Senior secondary school Govt. Girls Higher SEC. School Amira Kadal Srinagar(percentage 70%).

Secondary school Lawrence Vidya Bhawan Srinagar(percentage 92%).

SKILLS:

- Microsoft Office (EXCEL, WORD, POWERPOINT)
- QCA
- Team management.

SPECIALIZATIONS:

- B-Tech (ELECTRONICS & COMMUNICATIONS ENGINEERING), Rank 1 in class of 43

WORKSHOPS/ CONFERENCES ATTENDED:

- National conference on “EMERGING TRENDS & INNOVATION IN ELECTRONICS & COMMUNICATION ENGINEERING (ETIECE-2017)” on 26-27 August 2017 at BGSB University organized by the Department of ECE, SOET.
- Soft skills and Workplace Communication online course, under the ‘LAKSHYA’ programme by IIT Bombay.
- National Conference on “Recent Advances in Computer Science and IT(NCRACIT)”(24-25 April 2018)at BGSBU Rajouri
- International Conference on “INTELLIGENT COMPUTING and SMART COMMUNICATION (ICSC-2019)” on 19-21 April 2019 at THDC-IHET, Tehri.
- One week workshop on “*Machine learning: Tools and techniques*” organized by department of computer science and engineering and Information technology and engineering in collaboration with Aligarh Muslim University from 31st October to 4th November, 2018.
- One day workshop on Power Bi organized by “Be10x”.

INTERNSHIPS/PROJECTS:

- Six weeks summer training on IOT using Arduino from 6 June to 17 July, 2018 at CETPA INFOTECH PVT LTD.
- Major project on “DESIGN OF REVERSIBLE LOGIC BASED COMBINATIONAL CIRCUITS IN QUANTUM-DOT CELLULAR AUTOMATA NANOTECHNOLOGY”.

BOOK CHAPTER PUBLICATIONS:

- “*Logic Design and Modeling of an Ultra-Efficient 3x3 Reversible Gate for Nanoscale Applications*”, *Algorithms for Intelligent Systems(AIS)*, Springer Nature, pp 1433-1442, January 2020.
- “*Modeling and Logic Synthesis of Multifunctional and Universal 3x3 Reversible Gate for Nanoscale Applications*”, *Algorithms for Intelligent Systems(AIS)*, Springer Nature, pp 1423-1431, January 2020.
- “*Design of reversible universal and multifunctional gate-based 1-bit full adder and full subtractor in quantum dot cellular automata nanocomputing*”, *Journal of Nanophotonics 14(3)*, 036002(2 July 2020).

CONFERENCE PUBLICATIONS

- “*Design of combinational circuits using 3x3 IMG Reversible gate in quantum dot cellular automata for Nano-electronic applications*”, *Int conference on Smart energy and communication*, March 2020.
- ‘*Evolution of Mobile Wireless Communication Systems from 1G to 5G: A Comparative Analysis*,’ *International Journal Of Scientific Research in Computer Science, Engineering and Information Technology*, vol 4, Issue 1, March-April 2018.
- “*Implementation of full adder based on 3x3 reversible and universal gate in 4-dot-2electron quantum-dot cellular automata nanotechnology*”, *6th international conference on recent trends and advancement in engineering and technology*.

VOLUNTEER EXPERIENCE:

- Student volunteer, ETIECE 2017

PERSONAL INFORMATION:

Parentage: Bhat Abdul Majeed and Farhat Majeed.

Date of birth: 24 March 1997.

Marital status: unmarried.

Permanent address: Advent colony near graveyard Nowgam Srinagar, J&K.

DECLARATION:

I hereby declare that the above-mentioned information is true and best of my knowledge.

